

Abstract of the Disclosure

A memory arrangement is provided, which has a programmable memory and a first buffer memory associated with the programmable memory, to which buffer memory, in the case of a command access, at least one command following the accessed command is written. A
5 second buffer memory may also be provided, to which buffer memory, in the case of a data access, at least one datum following the accessed datum is written. Also provided is a method for reading from a memory arrangement during program execution, wherein presence of a command access or a data access recognized, and a command following the accessed command is written to a first buffer memory and a datum following the accessed datum is
10 written to a second buffer memory. Thus, in this manner, a separate buffer memory is described for data accesses, and thus the content of the buffer for command accesses is not overwritten or destroyed when data accesses occur during the program execution.

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